

Remarks

Claims 1 – 21 are pending and rejected. Claims 1, 3, 5, 6, 10, 11, 13, 15, and 16 have been amended. Claims 2, 4, 7 – 9, 12, 14, 17 – 19, and 21 have been canceled. Applicants traverses the rejection of claims 1, 3, 5, 6, 10, 11, 13, 15, 16, and 20 and requests allowance of claims 1, 3, 5, 6, 10, 11, 13, 15, 16, and 20.

The disclosure has been objected to due to a number of informalities. The specification has been amended to correct the informalities noted by the examiner (the status of some of the co-pending applications).

Claim 1 has been rejected under 35 U.S.C. 102(e) as being anticipated by US 6,021,132 to Muller et. al. Claim 1 has been amended to include the limitation "a co-processor comprising a plurality of state information buffers for storing state information, a plurality of context buffers for storing context information associated with a plurality of events and a plurality of data buffers for storing data associated with the plurality of events where the state information comprises a data buffer pointer pointing to one of the plurality of data buffers and a context buffer pointer pointing to one of the plurality of context buffers". No new matter has been added. Support for the amendment is found in the specification starting at page 20 line 24 through page 21 line 20, and figure 6.

Muller does not teach having a co-processor having state buffers, data buffers, and context buffers, where the state buffers store state information comprising a data buffer pointer pointing to a data buffer and a context pointer pointing to a context buffer. Therefore claim 1 is allowable as amended.

Claims 3, 5, and 6 depend on allowable claim 1. Therefore claims 3, 5, and 6 are also allowable.

Claim 10 has been rejected under 35 U.S.C. 102(e) as being anticipated by US 6,021,132 to Muller et. al. Claim 10 has been amended to include the limitation "the state information comprises a data only buffer pointer pointing to one of the plurality of data only information buffers and a context information buffer pointer pointing to one of the pluralities of context information buffers". Therefore the arguments for claim 1 (above) apply to claim 10. Therefore claim 10 is allowable as amended.

Claim 11 has been rejected under 35 U.S.C. 102(e) as being anticipated by US 6,021,132 to Muller et. al. Claim 11 has been amended to include the limitation "a co-

processor having a state information buffer for storing state information, a context buffer for storing context information and a data buffer for storing data where the state information comprises a data buffer pointer and a context pointer where the data buffer pointer points to the data buffer and the context pointer points to the context buffer". Therefore the arguments for claim 1 (above) apply to claim 11. Therefore claim 11 is allowable as amended.

Claims 13, 15, and 20 depend on allowable claim 11. Therefore claims 13, 15, and 20 are also allowable.

This application is now considered to be in condition for allowance and applicants respectfully request allowance of claims 1, 3, 5, 6, 10, 11, 13, 15, 16, and 20.



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